**ECCS 2311 Electric Circuits**

**Signal Conditioning Design Alternative Rubric**

**Due October 27, 2017**

* **Nodal Analysis with derivation of input-output mapping of Design A: 15%**
* **Nodal Analysis with derivation of input-output mapping of Design B: 15%**
* **Identification of constraints & evaluation metrics and cost analysis of Design A: 20%**
* **Identification of constraints & evaluation metrics and cost analysis of Design B: 20%**
* **Justification of Design Choice based on criteria (which Design is chosen and why): 10%**
* **PSPICE schematic, simulation setup description, and output voltage for Design A: 10%**
* **PSPICE schematic, simulation setup description, and output voltage for Design B: 10%**

 **Total: 100%**